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interconnect using
Cadence tools: the
Sigrity X PowerSI
hybrid solver, the full-
wave 3D finite ...

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**Conquer Signal and
Power Challenges
When Implementing
GDDR6 Interfaces**

Experience with Board
layout and SI
simulation tools viz.

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Cadence Sigrity, Ansys,
ADS etc. Experience
with Xilinx FPGA flow
and tool-kit (Vivado) ...

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