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As a result of increasing complexity of the VLSI designs, SoC verification engineers are shifting ... based on certain parameters defined in Architecture Design Document (ADD) and SoC guide, which can ...

Integrated Low Power Verification Suite: The way forward for SoC use-case Verification
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Further, the lab has extensive computer-aided design and simulation capability, including both commercial packages and research-grade in-house solvers. In both research and teaching, connections ...

CHAPTER 11: Department of Electrical and Computer Engineering
Design and implementation topics include sorting, searching, and graph algorithms. Design paradigms include ... and the other technologies that are central to MEMS fabrication. A study of the physical ...

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William Phillips Curriculum Vitae
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